

JCS97 U.S. PTO
09/904306

07/13/81

257	Class	666	Subclass	ISSUE CLASSIFICATION
-----	-------	-----	----------	----------------------

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. KS 563 SCANNED	PATENT DATE Q.A. JHT
-------------------------------	-------------------------

APPLICATION NO. 09/904306	CONT/PRIOR	CLASS 156 257	SUBCLASS 666+	ART UNIT 1733 2811	EXAMINER Q VU
------------------------------	------------	---------------------	------------------	--------------------------	------------------

APPLICANTS
Peter Nuytkens
Ilya Popeko
Joseph Kulinets

TITLE
Interconnect circuitry, multichip module, and methods of manufacturing thereof

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257	666	257	667	668	773	774			
INTERNATIONAL CLASSIFICATION									
H01L	23/495								

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 13	Figs. Drwg. 39	Print Fig. 39	Total Claims 14	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner)			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)			Amount Due	Date Paid
ISSUE BATCH NUMBER					
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.					

Form PTO-436A
(Rev. 6/99)
 FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
 (Attached in pocket on right inside flap)